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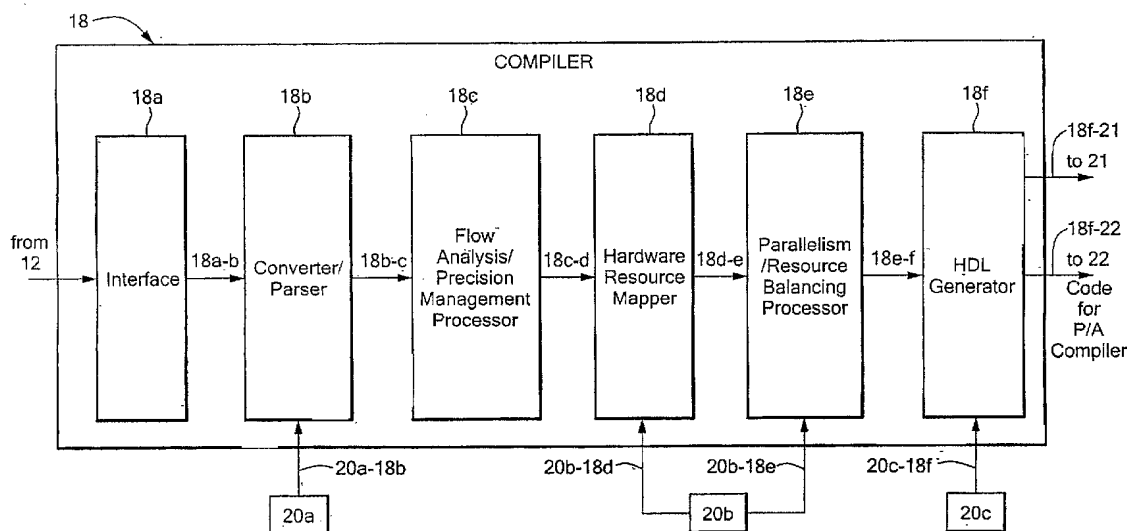
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[Continued on next page]

(54) Title: SYSTEM AND METHOD FOR PROGRAMMABLE LOGIC ACCELERATION OF DATA PROCESSING APPLICATIONS AND COMPILER THEREFORE



(57) Abstract: An accelerated processor for use in massive data manipulations specific to an application. A workstation has a general purpose processor and a coprocessor connection to which an application specific coprocessor system is connected. The coprocessor system has programming code which is assembled as instructions for the specific application in combination with accelerator environment specific requirements, independently provided but designed to allow non design experts to input application specific code.

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